U.S.S.N. 10/707,004 (DP-309884) - 2

LISTING OF THE CLAIMS:

- 1. 10. (canceled)
- 11. (currently amended) A method of developing an electronic module comprising a motherboard and a multichip module mounted to the motherboard, the method comprising the steps of:

providing a developmental unit of the electronic module, the developmental unit comprising the motherboard, the multichip module, and a circuit unit connected to the multichip module, the circuit unit comprising a flexible substrate, instrumentation circuitry required only in the developmental unit and mounted on the flexible substrate, and a connector coupled to the flexible substrate, the flexible substrate having signal lines that electrically communicate with the multichip module, the instrumentation circuitry, and the connector, a portion of the flexible substrate being between the multichip module and the motherboard and permitting electrical communication therebetween, the instrumentation circuitry not being mounted directly to the motherboard so as not to require space on the motherboard; and then

producing a production unit of the electronic module by eliminating the circuit unit without altering the motherboard.

- 12. (original) The method according to claim 11, wherein the step of providing the developmental unit comprises placing the motherboard, the multichip module, the flexible substrate, and the instrumentation circuitry in a housing, and mounting the instrumentation circuitry directly to the housing so as to be spaced apart from the motherboard.
- 13. (original) The method according to claim 12, wherein the step of providing the developmental unit further comprises supporting the connector outside the housing with the flexible substrate.

U.S.S.N. 10/707,004 (DP-309884) - 3

- 14. (original) The method according to claim 11, wherein the step of providing the developmental unit comprises mounting the instrumentation circuitry directly to the multichip module.
- 15. (original) The method according to claim 14, wherein the step of providing the developmental unit further comprises placing the motherboard, the multichip module, the flexible substrate, and the instrumentation circuitry in a housing, and supporting the connector outside the housing with the flexible substrate.
- 16. (original) The method according to claim 14, wherein the step of providing the developmental unit further comprises individually encasing the multichip module and the instrumentation circuitry in overmolded bodies.
- 17. (original) The method according to claim 11, wherein the step of providing the developmental unit comprises placing the motherboard, the multichip module, the flexible substrate, and the instrumentation circuitry in a housing, and attaching a portion of the flexible substrate to the housing so that the instrumentation circuitry is spaced apart from the motherboard.
- 18. (original) The method according to claim 17, wherein the step of providing the developmental unit further comprises supporting the connector outside the housing with the flexible substrate.
- 19. (original) The method according to claim 11, wherein the step of providing the developmental unit further comprises encasing the motherboard, the multichip module, the flexible substrate, and the instrumentation circuitry in an overmolded body, the instrumentation circuitry being suspended within the

U.S.S.N. 10/707,004 (DP-309884) - 4

overmolded body so as to be spaced apart from the motherboard, the connector projecting outside the overmolded body.

20. (original) The method according to claim 11, wherein the step of providing the developmental unit comprises mounting the instrumentation circuitry to a printed circuit board and attaching the printed circuit board to the flexible substrate.